

## CC-Link interface Identification Processor

CC-Link

BIS M series

BIS S series

BIS C series

■ The processor directly connects to CC-Link Network



BIS M-689-001  
 BIS S-404-30-KBT01  
 BIS C-489-1\_-KBT02

### Function

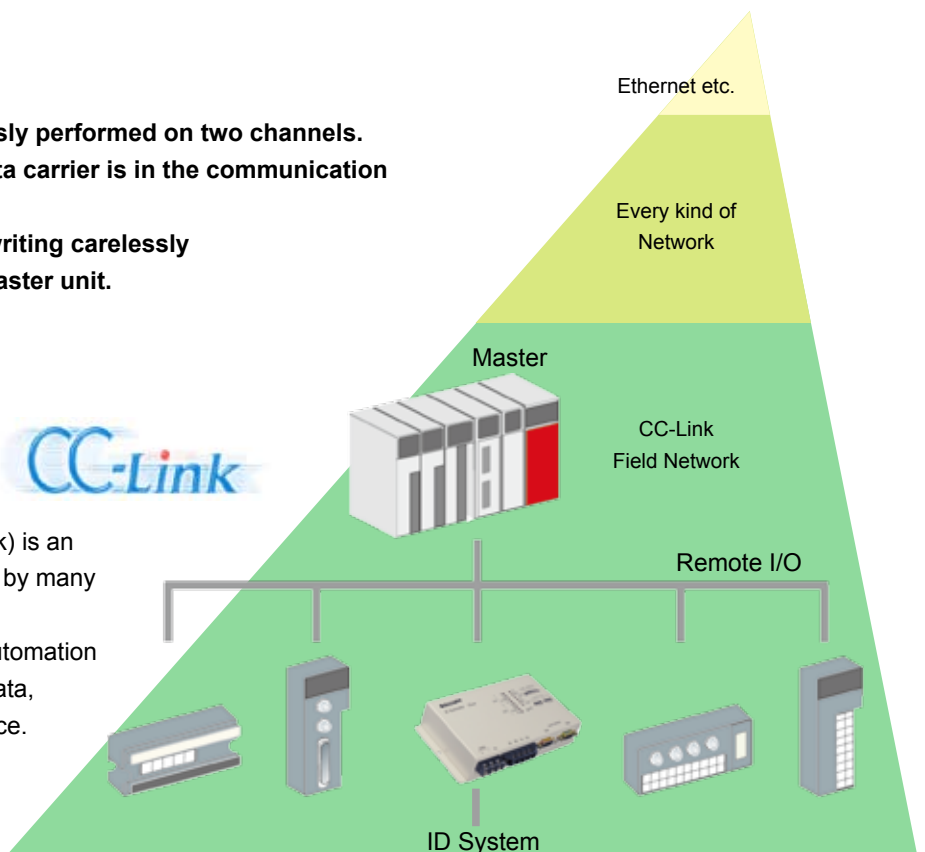
- Data transmission is simultaneously performed on two channels.
- Inzone signal is output when a data carrier is in the communication area of an antenna.
- Write protect function prevent rewriting carelessly
- Up to 16 units can connect to a master unit.

### About CC-Link

CC-Link (Control & Communication Link) is an open field network from Japan adopted by many manufactures.

This network is used in many factory automation facilities, that need a large amount of data, with simple wiring and good maintenance.

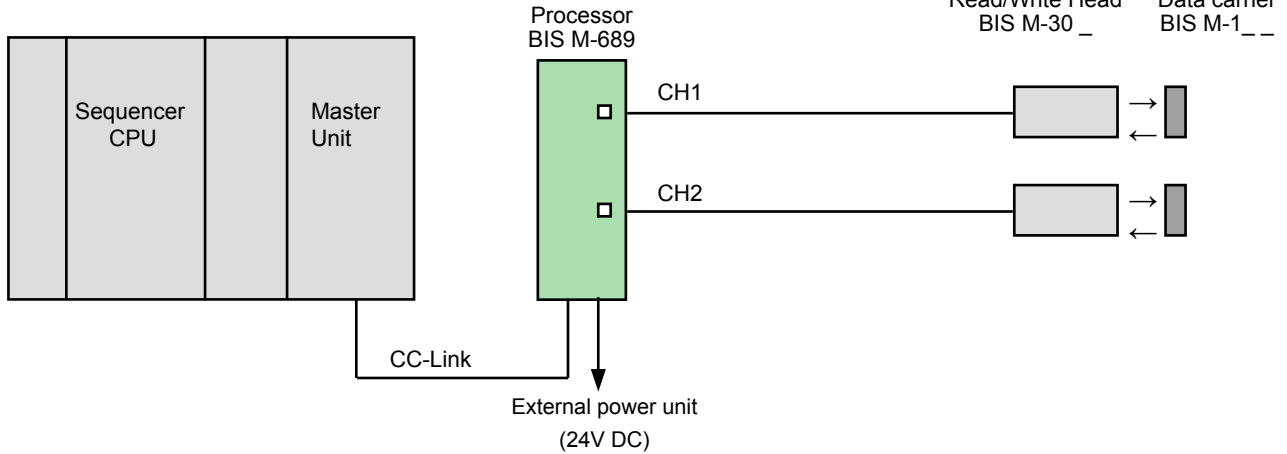
This processor is connected to CC-Link as a remote device station.



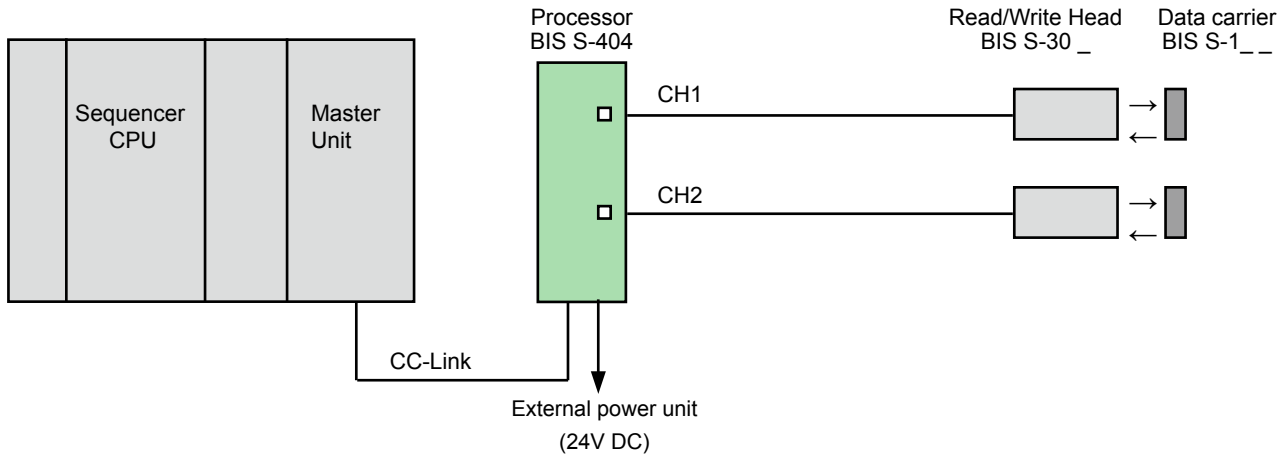
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Up to 16 units can be connected to a master unit.

### Construction of the System of the System

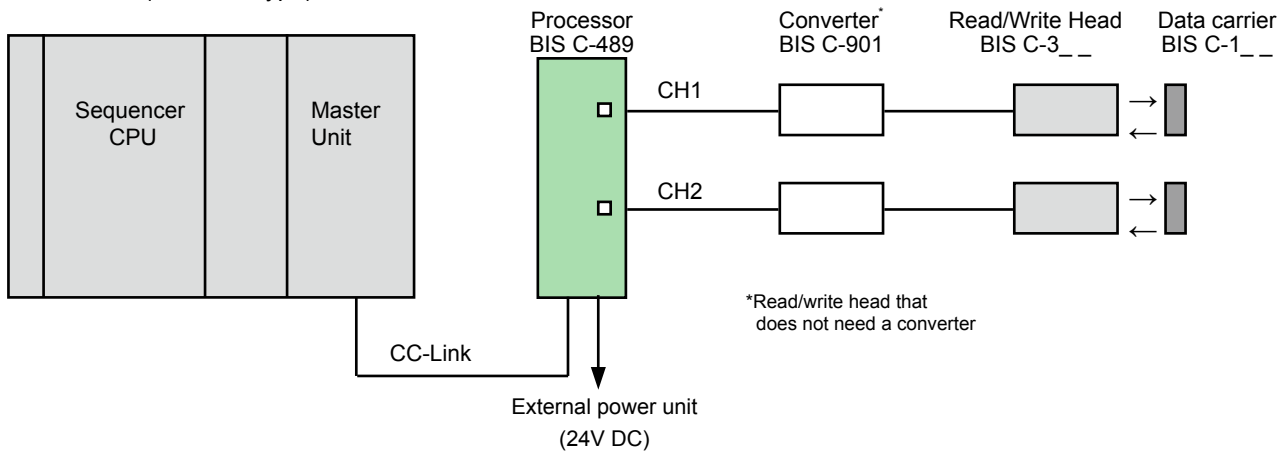
BIS M series(Low-cost ver.)



BIS S series(High-speed type)



BIS C series(Standard type)



#### Note for System construction

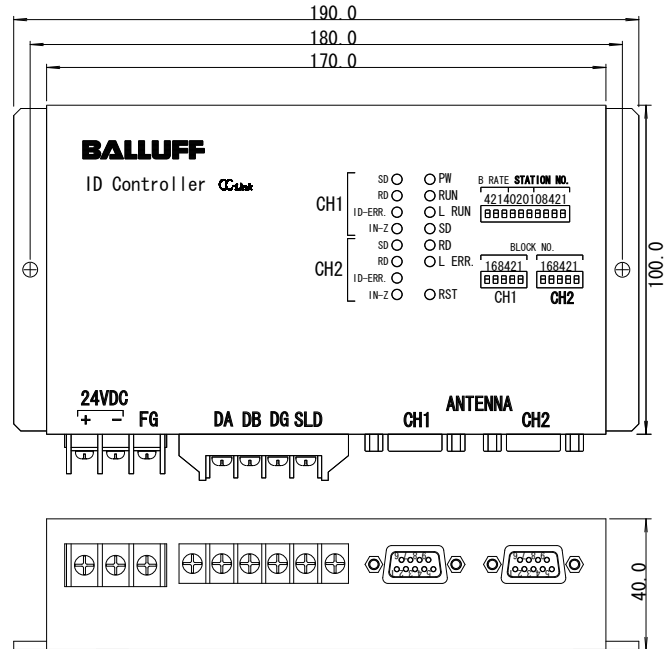
The occupied stations of Processor are 4 stations, therefore, plural processors can be connected to a master unit within the allowable number of station.

Please decide the number of processors to be connected to the master unit in considering the following points.

- (1) Maximum number of connectable stations for master unit is 16. (occupied 4 stations)
- (2) The number of occupied station of the units (input/output card etc.) except processors, and Input/Output occupied numbers.
- (3) Processing time as well as influence to tact time in programming (ladder).

## Specification / Dimension

|                              |         |
|------------------------------|---------|
| Connectable read/write heads | 2CH     |
| Interface                    | CC-Link |
| Dimension                    |         |



| Interface  | CC-Link                         |                                   |                                  |
|--|---------------------------------|-----------------------------------|----------------------------------|
| Applicable data carrier                            | BIS M series<br>(Low-cost ver.) | BIS S series<br>(High-speed type) | BIS C series<br>(Standard type)  |
| Type number  | BIS M-689-001 <sup>*1</sup>     | BIS S-404-30-KBT01                | BIS C-489-1_-KBT02 <sup>*2</sup> |
| Memory size  | 752byte                         | 8K byte                           | 511,1024,2047,8K byte            |
| Data processing time <sup>*3</sup> (Read / Write ) | 0.6 / 1.0 sec (Mifare)          | 0.5 / 0.9 sec                     | 1.4 / 4.3 sec                    |
|  | 0.7 / 1.1 sec ( ISO15693 )      |                                   |                                  |
| Supply voltage                                     | 24VDC +-10%                     |                                   |                                  |
| Current consumption                                | 0.8A                            |                                   |                                  |
| Operating temperature range                        | 0...+55 degree C                |                                   |                                  |
| Storage temperature range                          | -20...+75 degree C              |                                   |                                  |

\*1 : QD35ID1/2\* command compatible type : BIS M-688-002.

\*2 : ' \*2 ' part is different by memory size of Data carrier to use.

\*3 : Processing time from communication command is ON to finish of identification is ON  
/ 1 station per a master unit)

## Construction of components and applicable unit

The construction of components of CC-Link system that can be used the processor BIS M-689-001 , BIS S-404-30-KBT01 and BIS C-489-1\_-KBT02 applicable units are indicated the following. The processors cannot be connected to other units.

| CC-Link master / Local unit (sequencer CPU unit) |                |               |   |
|--|----------------|---------------|---|
| Sequencer<br>CPU unit                            | Type<br>Number | ACPU          | A1SHCPU / A1SJHCPU / A2SHCPU<br><br>A2UCPU / A2UCPU-S1 / A3UCPU / A4UCPU<br>(Software version Q or later. Version K or later, available only RRPA* function)<br><br>A2USCPU / A2USCPU-S1<br>(Software version E or later. Version A or later, available only RRPA* function)<br><br>A2USHCPU-S1<br>(Software version L or later. Version G or later, available only RRPA* function) |
|  |                | QnACPU        | Q02CPU / Q2ACPU-S1 / Q3ACPU / Q4ACPU / Q2ASCPU<br><br>Q2ASCPU-S1 / Q2ASHCPU / Q2ASHCPU-S1<br>(Function version B or later)  |
|  |                | QCPU (A mode) | Q02CPU-A / Q2HCPU-A / Q06HCPU-A   |
|  |                | QCPU (Q mode) | Q02CPU / Q2HCPU / Q06HCPU / Q12HCPU / Q25HCPU   |
| Master<br>Unit                                   | Type<br>Number |               | AJ61BT11 / A1SJ61BT11 / AJ61QBT11 / A1SJ61QBT11<br>(DATE indication 9707B or later)<br><br>QJ61BT11   |

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